

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Blaine D. Gaither et al

Confirmation No.: 2685

Application No.: 09/704176

Examiner: Elmore, Stephen C

Filing Date: Oct 31, 2000

Group Art Unit: 2186

Title: Cache Coherence Protocol For A Multiple Bus Multiprocessor System

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Mail Stop Amendment
Commissioner For Patents
PO Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment (X) Petition to extend time to respond
() New fee as calculated below () Supplemental Declaration
() No additional fee
() Other: _____ (fee \$ _____)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	7	MINUS	20	= 0	X \$18	\$ 0
INDEP. CLAIMS	3	MINUS	3	= 0	X \$88	\$ 0
[] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$300	\$ 0
EXTENSION FEE	1ST MONTH \$110.00	2ND MONTH \$430.00	3RD MONTH \$980.00	4TH MONTH \$1530.00		\$ 110
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 110

Charge \$ 110 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Blaine D. Gaither et al

By

Augustus W Winfield

Attorney/Agent for Applicant(s)
Reg. No. 34,046

Date: 10 22 2004

(X) I hereby certify that this paper is being transmitted
to the Patent and Trademark Office facsimile
number (703) 746-7239 on 10-22-2004
Number of pages: 7

Typed Name: Cathi Christensen

Signature: 

Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

ATTORNEY DOCKET NO. 10007099 -1

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Augustus W Winfield

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Date: 10 22 2004

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PATENT APPLICATION

ATTORNEY DOCKET NO. 10007099-1

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Blaine D. Gaither & Russ W. Herrell

Confirmation No. 2685

Serial No.: 09/704,176

Examiner: Elmore, Stephen C.

Filing Date: 10/31/2000

Group Art Unit: 2186

Title: CACHE COHERENCE PROTOCOL FOR A MULTIPLE BUS MICROPROCESSOR SYSTEM

COMMISSIONER FOR PATENTS
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RESPONSE TO OFFICE ACTION

REMARKS

No amendments have been made. Claims 1, 2, 4-6, 11, and 12 remain in the application. Reconsideration and reexamination is respectfully requested.

In the paper mailed 06/23/2004, the drawings were objected to because figure 5 includes the word "may", which the examiner alleges makes the drawing "vague and indefinite". Applicant traverses the objection. There is no legal basis in 37 C.F.R. or the MPEP for rejecting a drawing because the drawing is vague and indefinite.

In the paper mailed 06/23/2004, claims 1-6 were rejected under 35 U.S.C., second paragraph, as being indefinite, because of the phrase in claim 1: "for which a corresponding line in memory may not be identical". Applicant traverses. From MPEP 2171, the proper test for whether a claim is indefinite under 35 U.S.C., second paragraph, is whether the claim is clear to a hypothetical person possessing the ordinary level of skill in the pertinent art. From a plain English reading of the claim, without referring to the specification, the claim states that the corresponding line in memory might not be identical, or potentially is not identical. That uncertainty is what qualifies the line to have a reference